



ioNODE Series LoRa End Device RF Module

MiniMOD25

Datasheet (Preliminary)

Document Version:

SSTPL/HW/EDDS/MMD/2.5.1



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1. Brief Description

MiniMOD25 is a compact, low cost, low power wide area network (LPWAN) wireless module that supports the LoRaWAN long range wireless protocol.

This new stand-alone module measures just 28*23 mm, is constructed in a metal shielded package and comprises a Semtech SX1272 ultra long range spread spectrum wireless transceiver and STM32 series ARM Cortex-M0 + 32 bit microcontroller (MCU) along with High Performance Half Watt PA & Low Power LNA which makes the module ideal for high noise conditions. The MiniMOD25 module complies with the latest LoRaWAN Class A & C protocol specifications, it is simple to access LoRa-WAN IoT platforms.

1.1 Features

- Wide range of temperatures -40°C to +85°C.
- Normal RF output power up-to +26 dBm.
- It supports LoRa Point to Point communications as well as LoRaWAN protocol. Different Firmware required.
- Built-in EEPROM, data kept unchanged even powered off.
- Small size (28*23 mm)
- Wide range of working voltage 2.8V to 3.6 V, Option of 5V supply on different pin
- Sensitivity -148 dBm

1.2 Applications

Typical applications for this module include smart metering, wearables, tracking, M2M and internet of things (IoT) edge nodes.

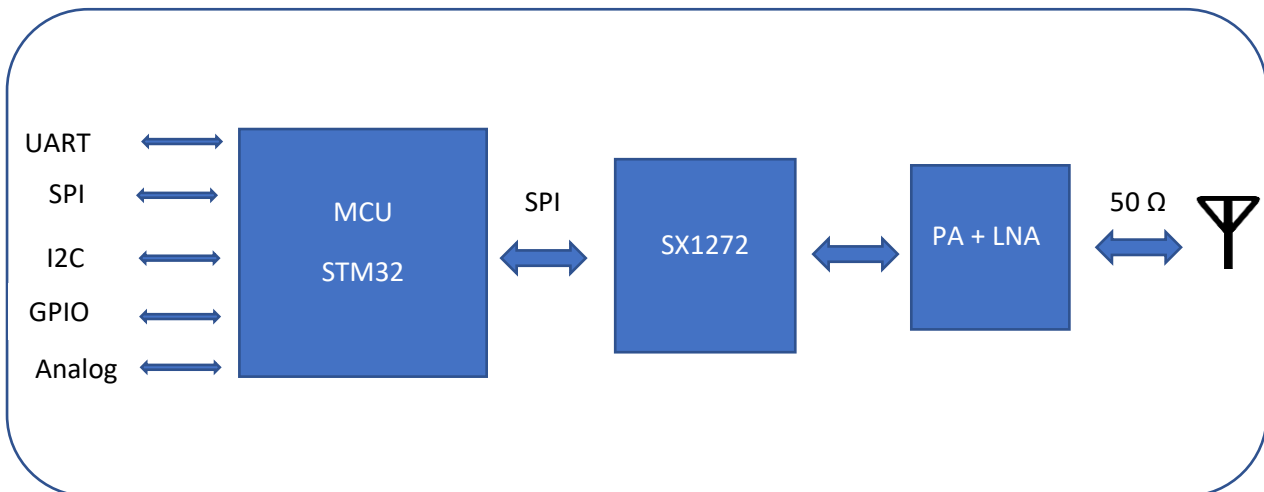
The module's applications are as following -

- Automated Meters Reading
- Home and Building Automation
- Wireless Alarm and Security Systems
- Industrial Monitoring and Control

- Long Range Irrigation Systems

2. Module Overview:

MiniMOD_2.5 is an ultra-long range, high-performance, RF module for wireless communication. It operates in the license free 865-867 MHz ISM frequency band and includes all necessary passive components for wireless communication as depicted in the following figure.



Frequency range	865 to 867 MHz
Modulation	LoRa® Spread-Spectrum
RF output power	up to +26 dBm
Receiver sensitivity	-148 dBm (SF 12; SB 125 kHz, CR 4/6)
RF Datarate	0.24 to 5 kbps
RF range	up to 7000 m (line of sight)
Operating voltage	2.8 V to 3.6 V, Separate 5V optional
Current consumption	< 15 μ A (module in sleep, RTC running) 50 mA (Rx) 390 mA (Tx mode)
Interfaces	UART, SPI, I2C
IO's	Digital IOs Analog Inputs
Dimension	28x23 mm
Operating temperature	-40°C to +85°C

3. Electrical Characteristics

3.1 Maximum Ratings

Condition	Min	Typ.	Max	Unit
Supply Voltage (VDD)	2.8	3.3	3.6	V
Storage Temperature	-40	+25	+85	°C
Operating Temperature	-40	+25	+85	°C
RF Input Power	-5			dBm
ESD (Human Body Model)	2000			V
ESD (Charge Device Model)	500			V
Notes:				
1) Unless otherwise noted, all voltages are with respect to GND				

3.2 General Electrical Characteristics

T = 25°C, VDD = 3.3 V (typ.) if nothing else stated					
Parameter	Condition	Min	Typ.	Max	Unit
Supply Voltage (VDD)		2.8	3.3	3.6	V
Current Consumption System IDLE	TRX idle mode, μ C idle mode		15		μ A
Current Consumption RECEIVE LoRa	TRX receive mode, μ C sleep mode		50		mA
Current Consumption TRANSMIT	TRX transmit mode, μ C sleep mode, all μ C units off, max. RF power level		390		mA
MCU operation frequency		32 MHz & 32.768 KHz			

3.3 Module Interface Characteristics

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
V_{IL}	Input low level voltage	TC, FT, FTf, RST I/Os	-	-	$0.3V_{DD}$	V
		BOOT0 pin	-	-	$0.14V_{DD}^{(1)}$	
V_{IH}	Input high level voltage	All I/Os	$0.7 V_{DD}$	-	-	
V_{hys}	I/O Schmitt trigger voltage hysteresis ⁽²⁾	Standard I/Os	-	$10\% V_{DD}^{(3)}$	-	
		BOOT0 pin	-	0.01	-	

Symbol	Parameter	Conditions	Min	Max	Unit
$V_{OL}^{(1)}$	Output low level voltage for an I/O pin	CMOS port ⁽²⁾ , $I_{IO} = +8 \text{ mA}$ $2.7 \text{ V} \leq V_{DD} \leq 3.6 \text{ V}$	-	0.4	V
$V_{OH}^{(3)}$	Output high level voltage for an I/O pin		$V_{DD}-0.4$	-	
$V_{OL}^{(1)}$	Output low level voltage for an I/O pin	TTL port ⁽²⁾ , $I_{IO} = +8 \text{ mA}$ $2.7 \text{ V} \leq V_{DD} \leq 3.6 \text{ V}$	-	0.4	
$V_{OH}^{(3)(4)}$	Output high level voltage for an I/O pin	TTL port ⁽²⁾ , $I_{IO} = -6 \text{ mA}$ $2.7 \text{ V} \leq V_{DD} \leq 3.6 \text{ V}$	2.4	-	
$V_{OL}^{(1)(4)}$	Output low level voltage for an I/O pin	$I_{IO} = +15 \text{ mA}$ $2.7 \text{ V} \leq V_{DD} \leq 3.6 \text{ V}$	-	1.3	
$V_{OH}^{(3)(4)}$	Output high level voltage for an I/O pin	$I_{IO} = -15 \text{ mA}$ $2.7 \text{ V} \leq V_{DD} \leq 3.6 \text{ V}$	$V_{DD}-1.3$	-	
$V_{OL}^{(1)(4)}$	Output low level voltage for an I/O pin	$I_{IO} = +4 \text{ mA}$ $1.65 \text{ V} \leq V_{DD} < 3.6 \text{ V}$	-	0.45	
$V_{OH}^{(3)(4)}$	Output high level voltage for an I/O pin	$I_{IO} = -4 \text{ mA}$ $1.65 \text{ V} \leq V_{DD} \leq 3.6 \text{ V}$	$V_{DD}-0.45$	-	
$V_{OLFM+}^{(1)(4)}$	Output low level voltage for an FTf I/O pin in Fm+ mode	$I_{IO} = 20 \text{ mA}$ $2.7 \text{ V} \leq V_{DD} \leq 3.6 \text{ V}$	-	0.4	
		$I_{IO} = 10 \text{ mA}$ $1.65 \text{ V} \leq V_{DD} \leq 3.6 \text{ V}$	-	0.4	

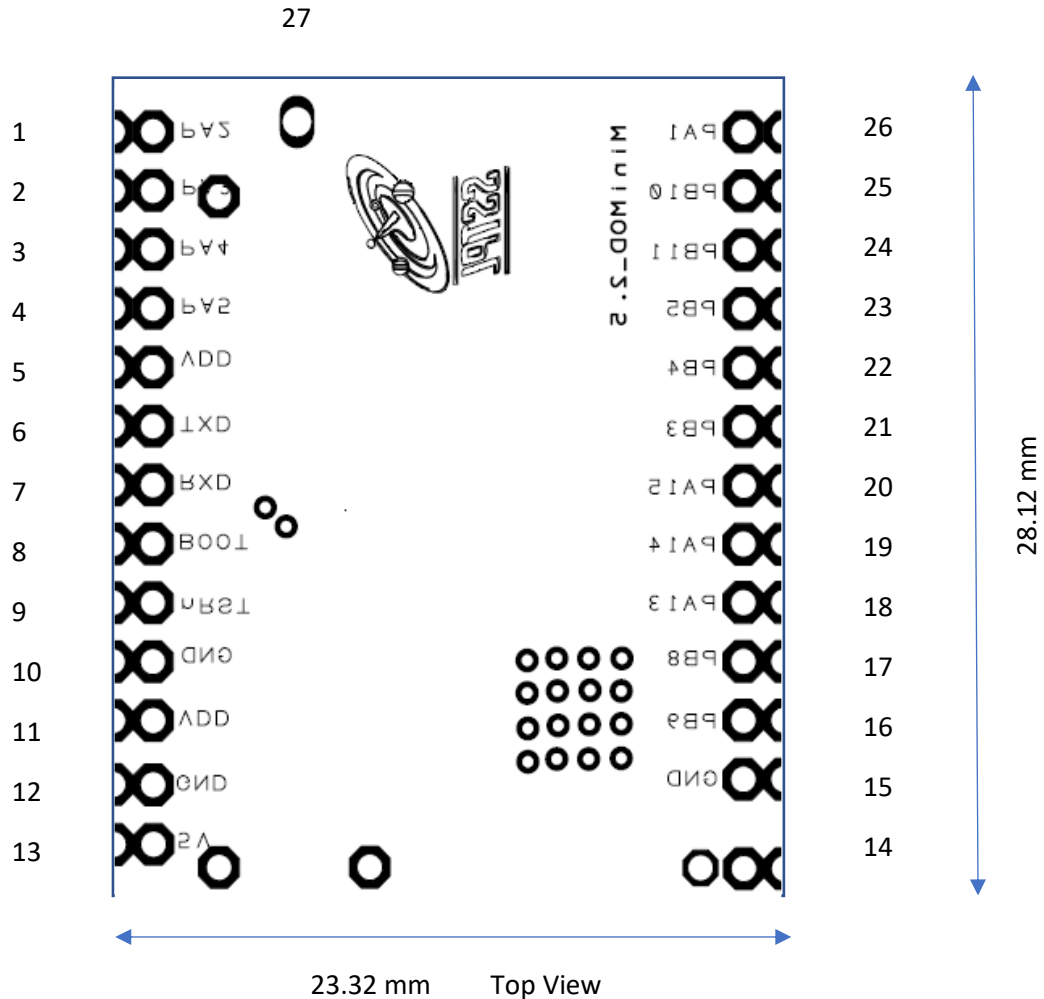
3.4 Transmitter RF Characteristics

T = 25°C, VDD = 3 V (typ.), 866 MHz if nothing else stated					
Parameter	Condition	Min	Typ.	Max	Unit
Frequency Range		865	-	867	MHz
RF Output Power - 865 MHz Band		24.0	25	26	dBm
Modulation Techniques		LoRaTM			
TX Frequency Variation vs. Temperature	-40 to +85°C	-	±10	-	kHz

TX Power Variation vs. Temperature	-	±0.5	-	dB
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4. Module Package

4.1 Module Dimension



4.2 Pinout Description

PIN	PIN Name	PIN Type	MCU Pin (number)	5 V Tolerance	Description
1	PA3	D I/O	PA3	Yes	ADC IN3
2	GND	Supply			Ground connection
3	PA4	D I/O	PA4	Yes	SPI1_NSS
4	GND	Supply		No	Ground connection
5	VDD1	Supply		No	Main Supply
6	TXD	D I/O	PA9	No	Digital IO / USART1-TX
7	RXD	D I/O	PA10	No	Digital IO / USART1-RX

8	Boot	D IN	BOOT0	No	Bootloader Pin 0, internally pulled-down by 47 kΩ
9	nRst	D IN	NRST	NO	NReset, internally pulled-up by 47 kΩ
10	GND	Supply			Ground connection
11	PB11	D I/O	PB11	Yes	LPUART1_RX/ LPUART1_TX/ I2C2_SDA (Ext. pullup required)
12	PB10	D I/O	PB10	Yes	LPUART1_TX/ LPUART1_RX/ I2C2_SCL (Ext. pullup required)
13	VddO	Supply			3.3V out up-to 20 mA
14	GND	Supply			Ground connection
15	5V	Supply			5V input to 3.3V LDO
16	PA13	D I/O	PA13	Yes	SWDIO
17	PA14	D I/O	PA14	Yes	SWCLK
18	VDDO	Supply			3.3V out up-to 20 mA
19	VDDO	Supply			3.3V out up-to 20 mA
20	GND	Supply			Ground connection
21	PA12	D I/O	PA12	Yes	Digital IO
22	PB3	Supply	PB3	Yes	SPI1_SCK/ USART5_TX
23	PB4	D I/O	PB4	Yes	SPI1_MISO/ USART5_RX
24	PB5	D I/O	PB5	Yes	SPI1_MOSI
25	PB8	D I/O	PB8	Yes	I2C1_SCL
26	PB9	D I/O	PB9	Yes	I2C1_SDA
27	GND	Supply			Ground connection
28	RF	A IN/OUT			External 50Ω port for antenna connection
29	GND	Supply		No	Ground connection

5. Antenna Mounting Options

To serve the propose of multiple antenna requirement, MiniMOD_2.5 comes with multiple options. Below are the MiniMOD_2.5 Antenna options:

1. Connect an external antenna thru the UFL connector mounted on PCB. It can be Simply connected thru a UFL Pigtail to antenna of your choice. Here 50Ω impedance matched antenna will work.
2. Through Hole Spring Helical antenna can also be used if antenna needs to be integral part of module. Just unmount the UFL connector and used through hole beneath the UFL connector pad to connect Spring Helical or other wire antenna. The recommend thick ness of wire is 0.7 to 0.9 mm to get it mounted in the PCB hole.
3. MiniMOD_2.0 has edge half cut pads to extend the RF signals on Daughter Board PCB and antenna can be mounted separately on DB PCB. Here needs to take care the 50Ω impedance characteristics of the RF track.

6. Important Notice

6.1 Disclaimer

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